IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Peyman Hadizad

FILED:

Concurrently Herewith

FOR:

VERTICAL COMPOUND SEMICONDUCTOR

FIELD EFFECT TRANSISTOR STRUCTURE

Date:

7/18/2003

DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.56

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted,

Peyman Hadizad

ON Semiconductor Intellectual Property Dept. P.O. Box 62890 - A700

Phoenix, AZ 85062-2890

James J. Stipanuk Reg. #44,358

Phone: Fax: 602-244-4885 602-244 3169

Customer #: 27255

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED

EXAMINER